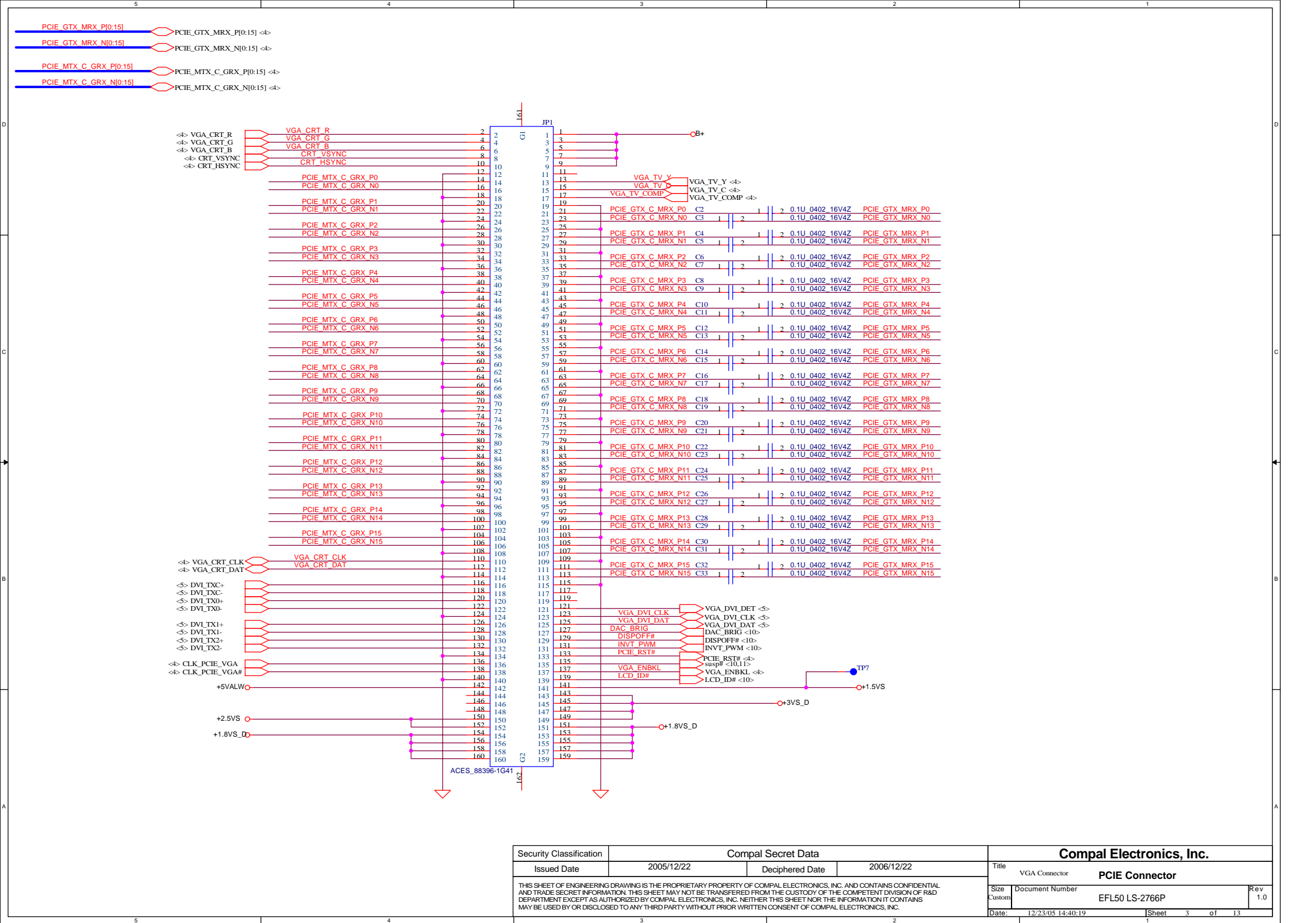


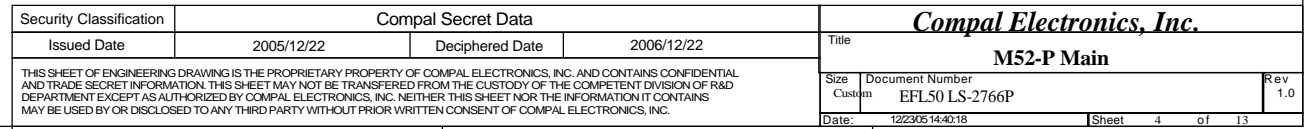
Mini (EFL50) ATI VGA/B M52-P

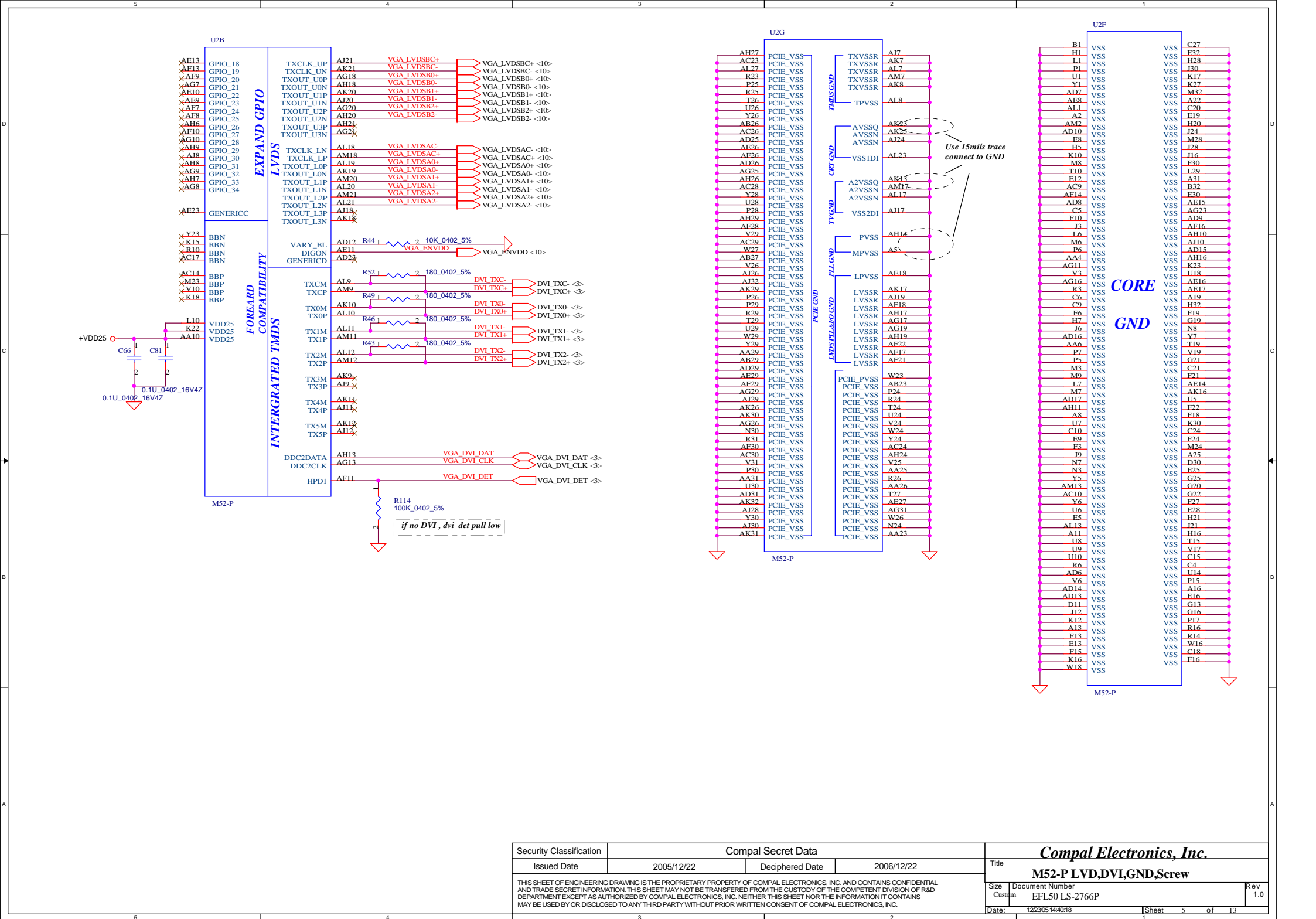
Revision 1.0

|  |            |                    |            |                          |            |
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| Date: 122305 14:40:19  |            | Sheet 1 of 13      |            |                          |            |



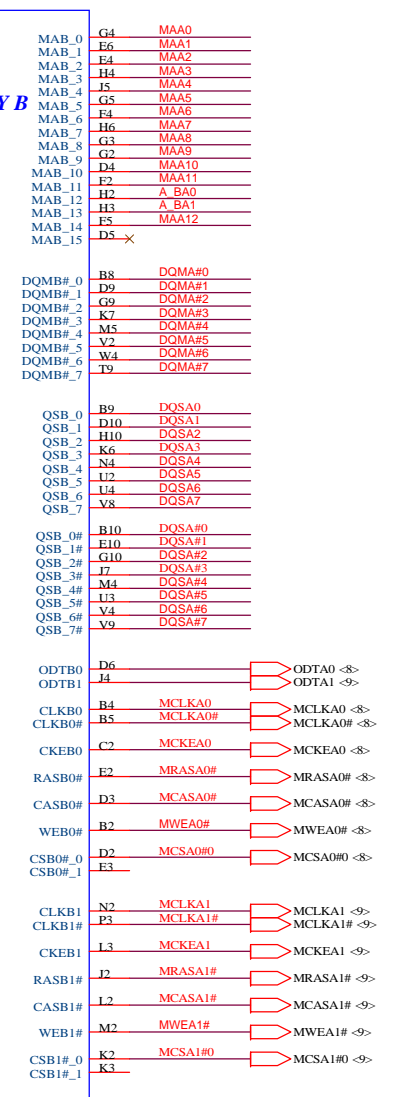
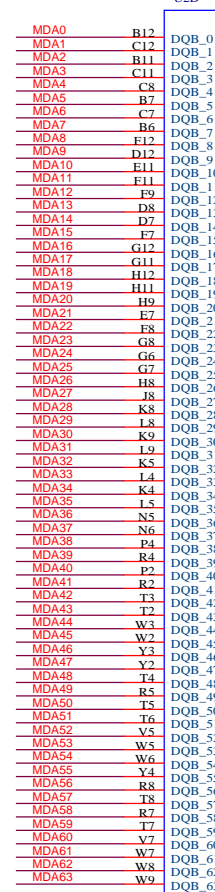
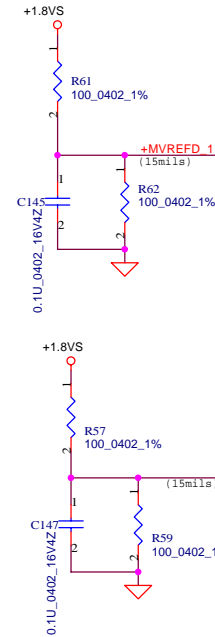
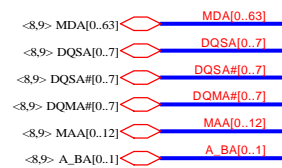
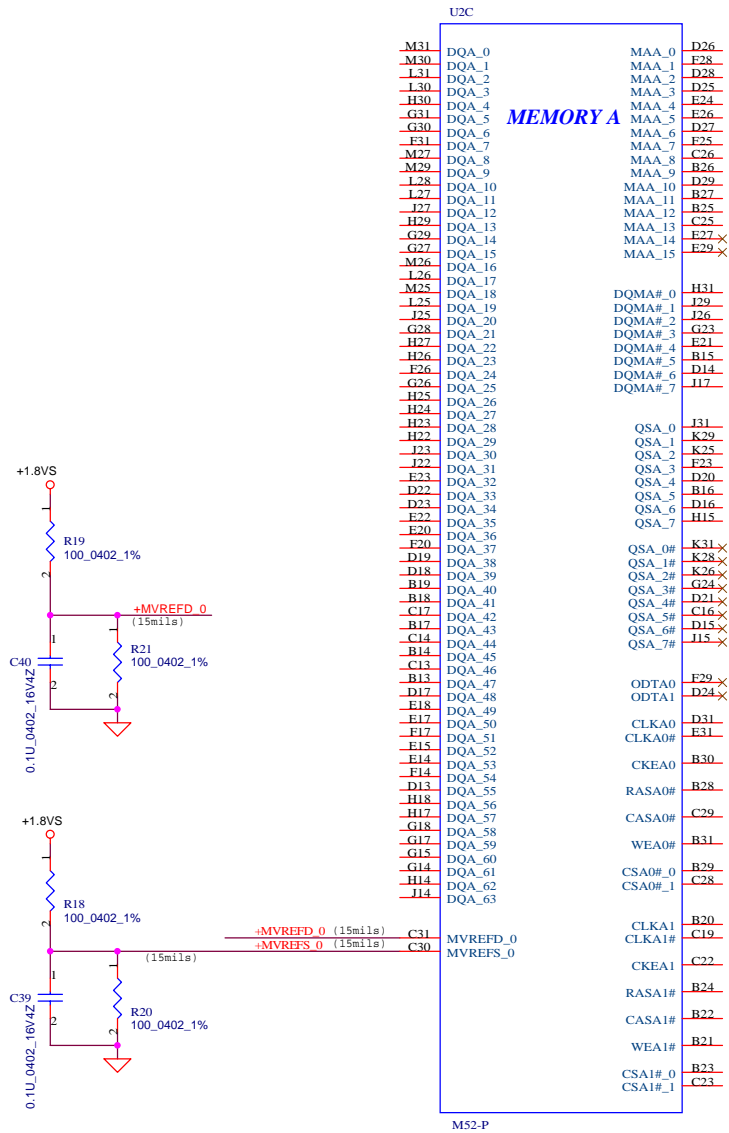


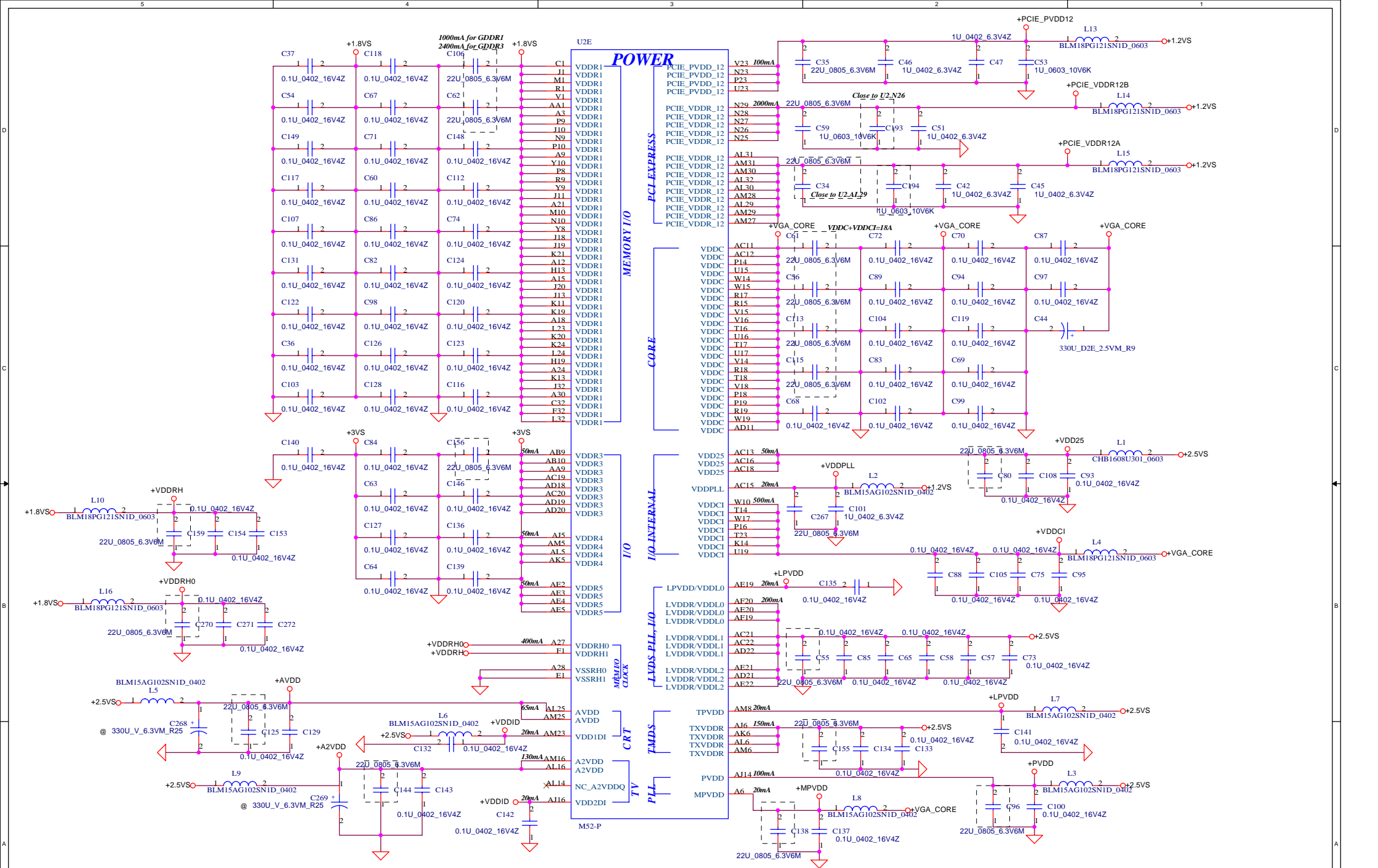




# GDDR2

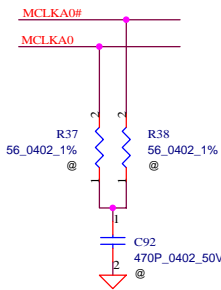
|   |            |                    |            |                        |                                   |
|---|------------|--------------------|------------|------------------------|-----------------------------------|
| Security Classification   |            | Compal Secret Data |            | Title                  |                                   |
| Issued Date   | 2005/12/22 | Deciphered Date    | 2006/12/22 | M52-P Memory Interface |                                   |
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| Date: 12/23/05 14:40:18   |            |                    |            | Sheet                  | 6 of 13                           |





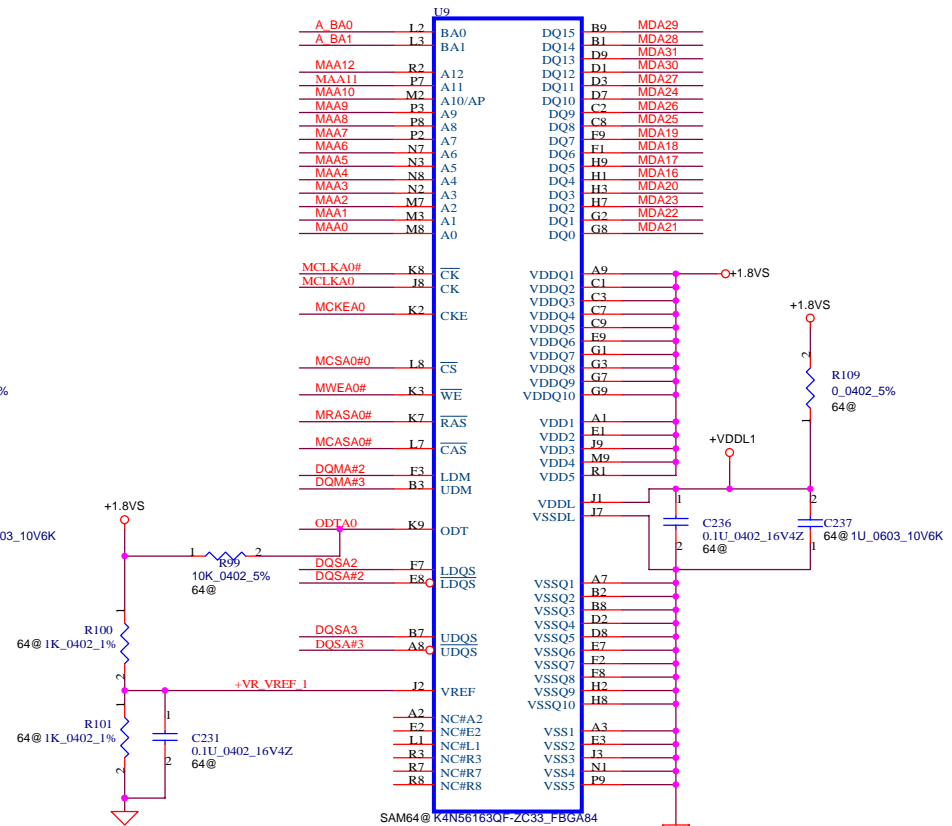
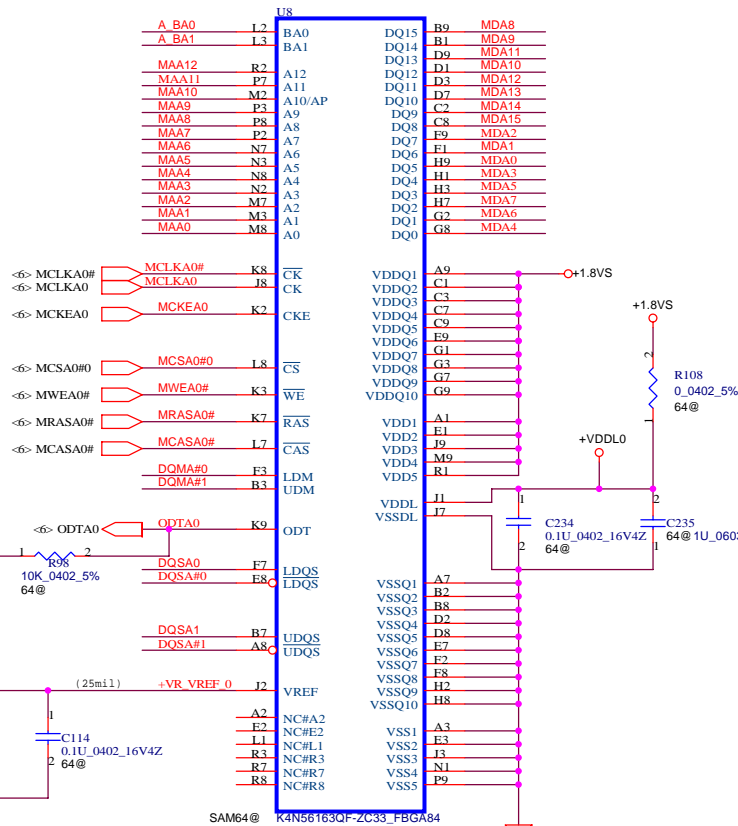
|   |            |                    |            |                                 |                 |               |
|---|------------|--------------------|------------|---------------------------------|-----------------|---------------|
| Security Classification   |            | Compal Secret Data |            | <b>Compal Electronics, Inc.</b> |                 |               |
| Issued Date   | 2005/12/22 | Deciphered Date    | 2006/12/22 | Title<br><b>M52-P Power</b>     |                 |               |
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|   |            |                    |            | Custom                          | EFL50 LS-2766P  | 1.0           |
|   |            |                    |            | Date: 12/23/05 14:40:19         |                 | Sheet 7 of 13 |



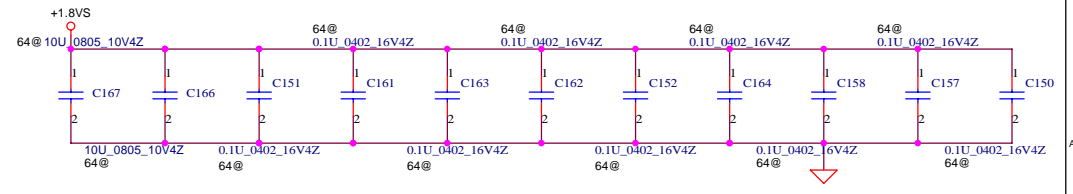
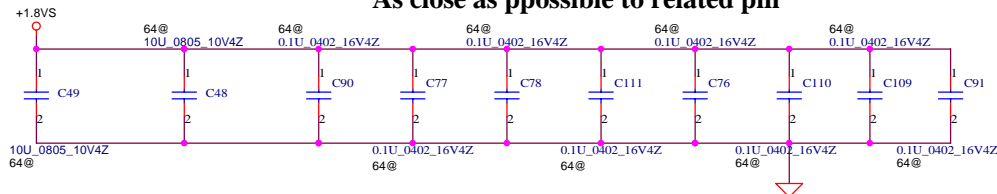


<6,9> MDA[0..63] MDA[0..63]  
<6,9> DQSA[0..7] DQSA[0..7]  
<6,9> DQSA# [0..7] DQSA# [0..7]  
<6,9> DQMA# [0..7] DQMA# [0..7]  
<6,9> MAA[0..12] MAA[0..12]  
<6,9> A\_BA[0..1] A\_BA[0..1]

Default VRAM chip is SAM: SA00000YC00.

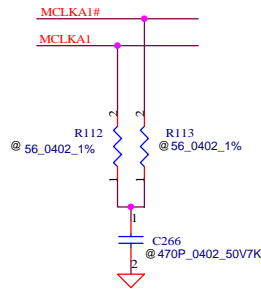


As close as ppossible to related pin

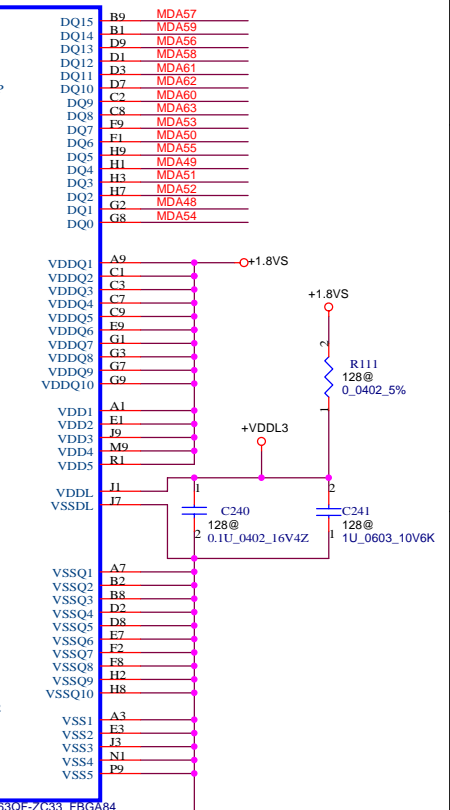
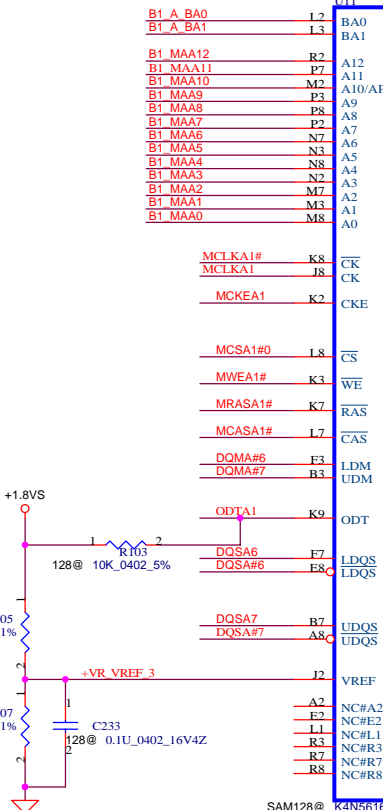
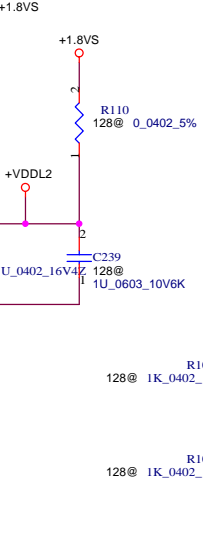
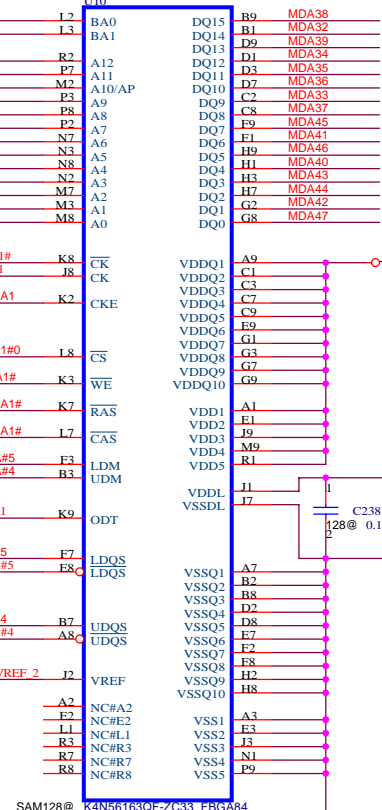
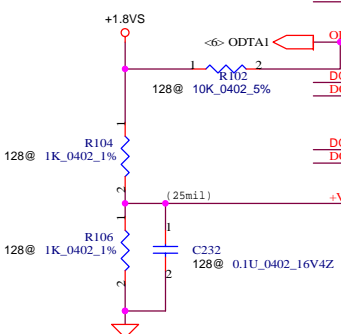
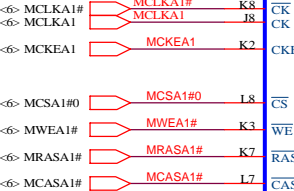
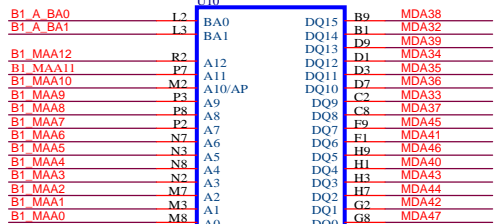
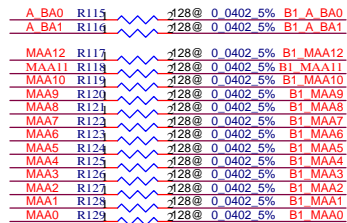


|   |            |                    |            |                   |                 |
|---|------------|--------------------|------------|-------------------|-----------------|
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|   |            |                    |            | Sheet             | 8 of 13         |



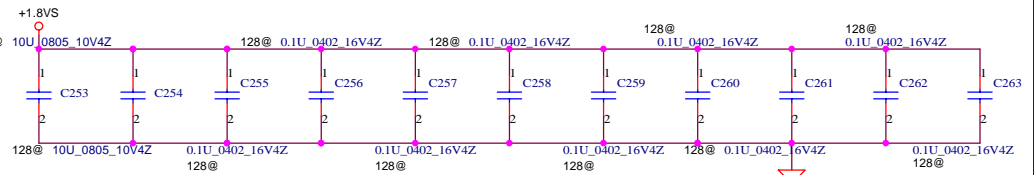
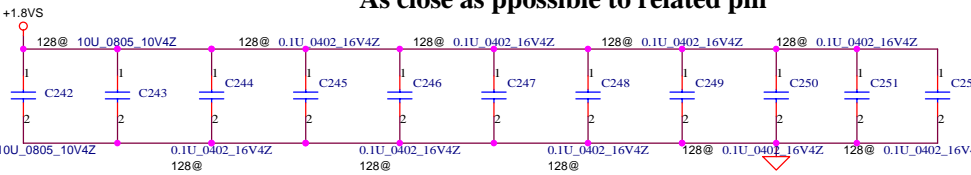


<6,8> MDA[0..63] MDA[0..63]  
<6,8> DQSA[0..7] DQSA[0..7]  
<6,8> DQSA#[0..7] DQSA#[0..7]  
<6,8> DQMA#[0..7] DQMA#[0..7]  
<6,8> MAA[0..12] MAA[0..12]  
<6,8> A\_BA[0..1] A\_BA[0..1]

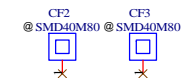
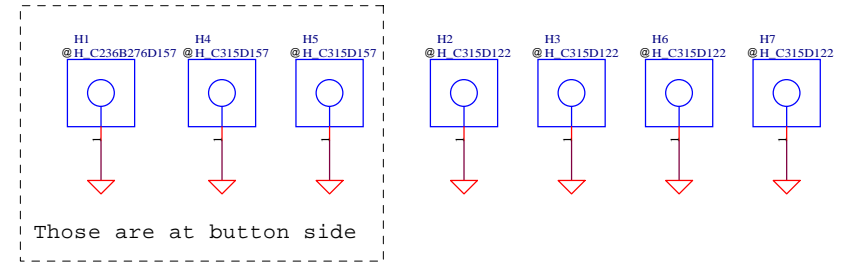
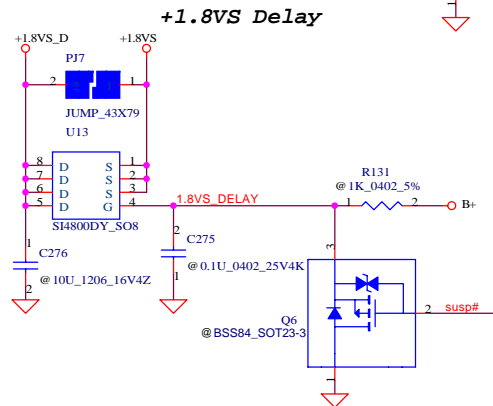
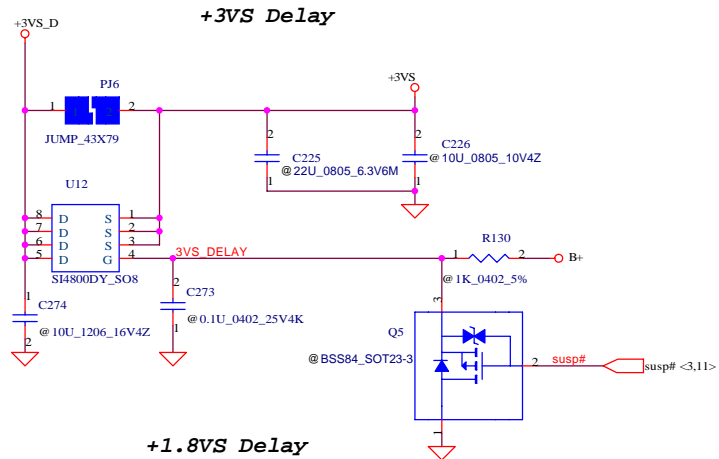
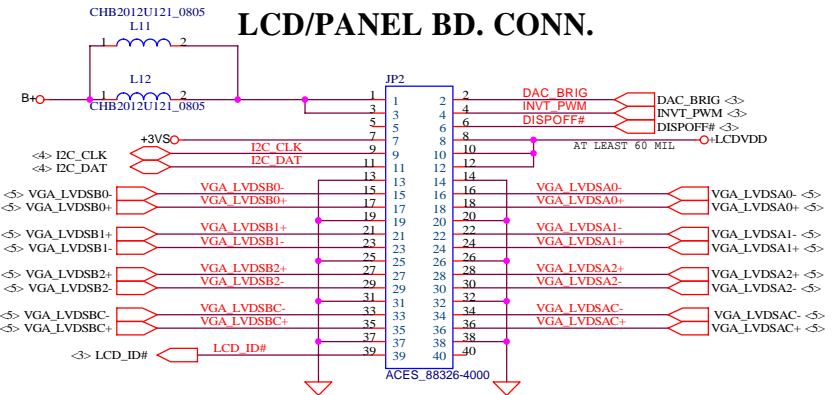
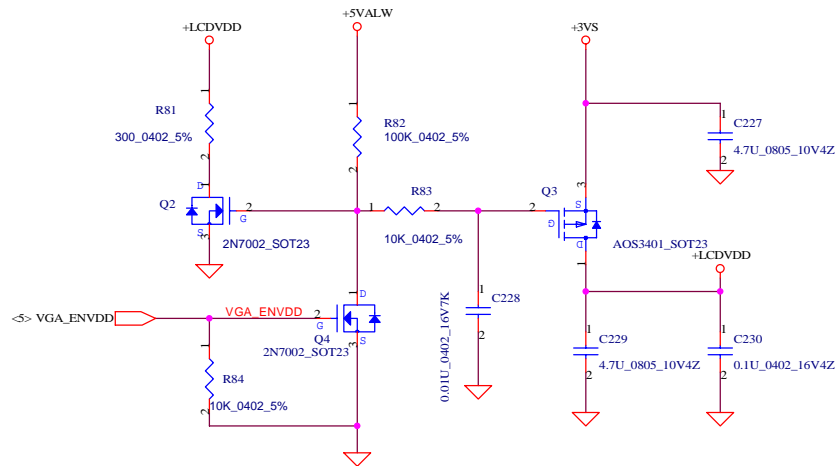


Default VRAM chip is SAM: SA00000YC00.

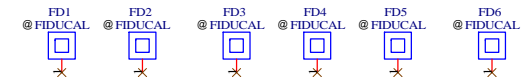
As close as ppossible to related pin



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| Security Classification   |  | Compal Secret Data |                 | <b>Compal Electronics, Inc.</b> |                   |                 |               |
| Issued Date   |  | 2005/12/22         | Deciphered Date | 2006/12/22                      | Title             |                 |               |
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|   |  |                    |                 |                                 | Custom            | EFL50 LS-2766P  | 1.0           |
|   |  |                    |                 |                                 | Date:             | 122305 14:40:19 | Sheet 9 of 13 |

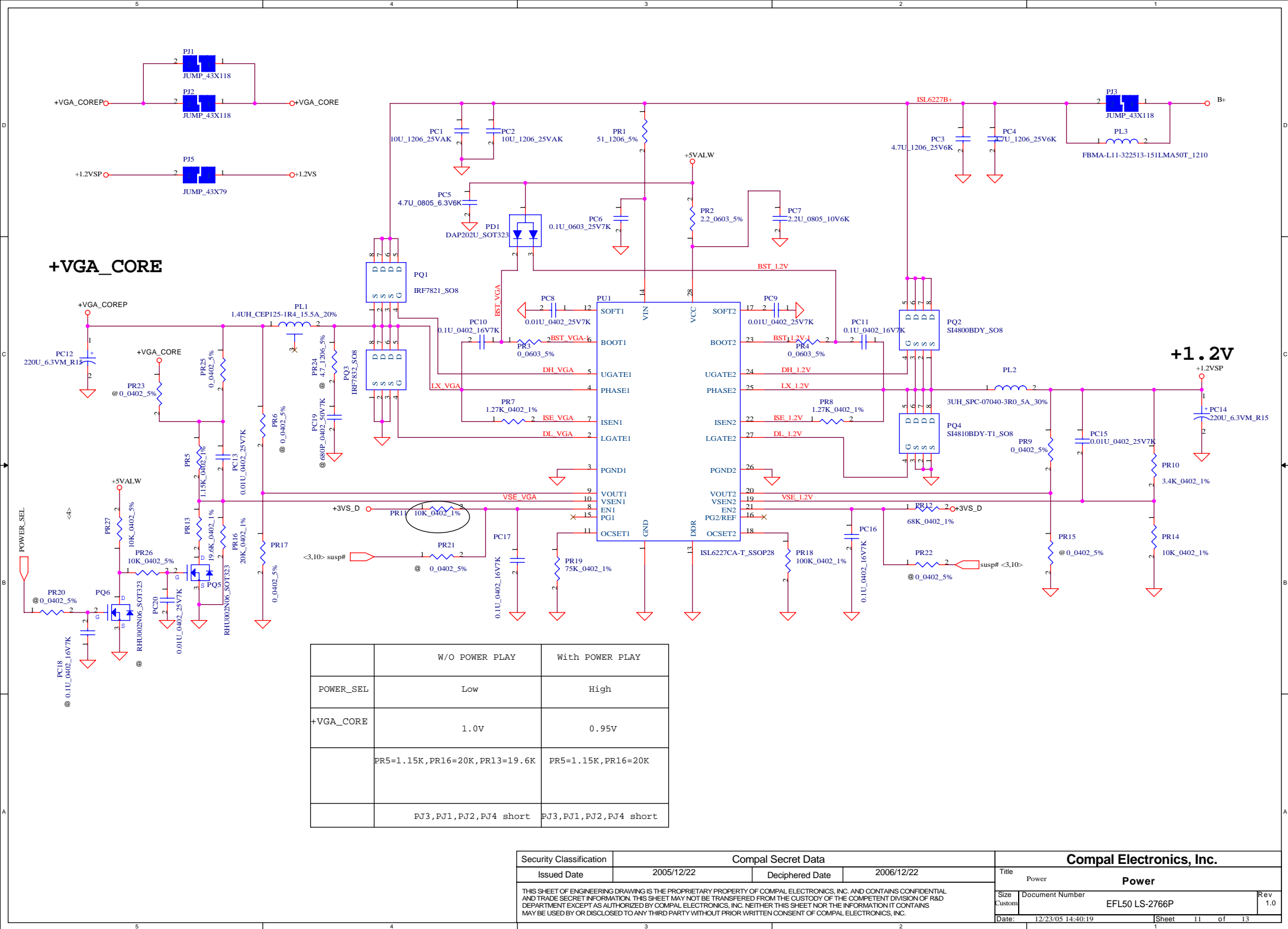


For IC FIXED



For Board FIXED

|   |            |                    |            |                          |                       |
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|   |            |                    |            | Sheet                    | 10 of 13              |
|   |            |                    |            | Rev                      | 1.0                   |



Version change list (P.I.R. List)

===== 2005/12 R0.2 =====

01. Reserve +3VS and +1.8VS delay circuit and reserve Jump. == To meet ATI PWR sequence.  
Reserve PJ6, PJ7, U12, U13, C273, C274, C275, C276,  
Q5, Q6, R130 and R131.
02. Change C42, C45, C46, C47 and C51, from 0.1U to 1U. == For 3D hang issue.
03. Change C101, from 0.1U to 1U. == For 3D hang issue.
04. Add C44 as 330U == For 3D hang issue.
05. Delete R63, 10K\_0402\_5% == PCIE TX power saving.

|  |            |                    |            |                          |                                   |
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|  |            |                    |            | Date: 122305 14:40:19    | Rev 1.0                           |
|  |            |                    |            | Sheet 12 of 13           |                                   |

ZZZ  
DA800005F10  
--PCB ZKD LS-2766P  
REV1 VGA/B

DA800005F10

U8  
SA00000JW00  
--S IC D2 16M16/600  
HY5PS561621AFP-33  
FBGA

HYN64@ SA00000JW00

U9  
SA00000JW00  
--S IC D2 16M16/600  
HY5PS561621AFP-33  
FBGA

HYN64@ SA00000JW00

U10  
SA00000JW00  
--S IC D2 16M16/600  
HY5PS561621AFP-33  
FBGA

HYN128@ SA00000JW00

U11  
SA00000JW00  
--S IC D2 16M16/600  
HY5PS561621AFP-33  
FBGA

HYN128@ SA00000JW00

|  |            |                    |            |                          |                                   |
|--|------------|--------------------|------------|--------------------------|-----------------------------------|
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|  |            |                    |            | Date:                    | 122305 14:40:19                   |
|  |            |                    |            | Sheet                    | 13 of 13                          |